

<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Not for submission under 37 CFR 1.99)	Application Number		10675721
	Filing Date		2003-09-30
	First Named Inventor	Levine et al.	
	Art Unit	2191	
	Examiner Name	Vo, Ted T.	
	Attorney Docket Number	AUS920030483US1	

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	2	4316245		1982-02-16	Luu et al.	
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12	6351844	B1	2002-02-26	Bala	
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20	6446029	B1	2002-09-03	Davidson et al.	
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	3	20030014741	A1	2003-01-16	Megiddo et al.	
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	2	YANG et al., "Improving Performance by Branch Reordering", Proceedings of the ACM SIGPLAN 1998 Conference on Programming Language Design and Implementation, Montreal Canada, 1008, pp. 130-141.	<input type="checkbox"/>
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	5	FISHER, "Trace Scheduling: A Technique for Global Microcode Compaction", IEEE Transactions on Computers, Vol. C30, No. 7, July 1981, pp. 478-490.	<input type="checkbox"/>
	6	CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321.	<input type="checkbox"/>
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	8	AHO et al., "Compilers: Principles, Techniques, and Tools", Addison-Wesley, 1988, pp. 488-497.	<input type="checkbox"/>

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- ☒ Fee set forth in 37 CFR 1.17 (p) has been submitted herewith.
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A signature of the applicant or representative is required in accordance with CFR 1.33, 10.18. Please see CFR 1.4(d) for the form of the signature.

Signature	/Gerald H. Glanzman/	Date (YYYY-MM-DD)	2007-03-30
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